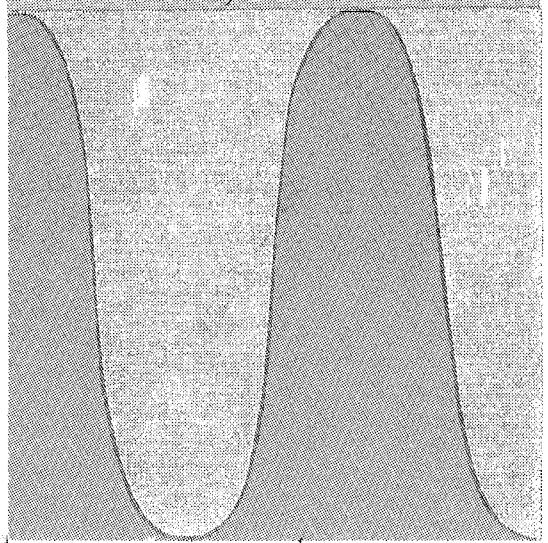
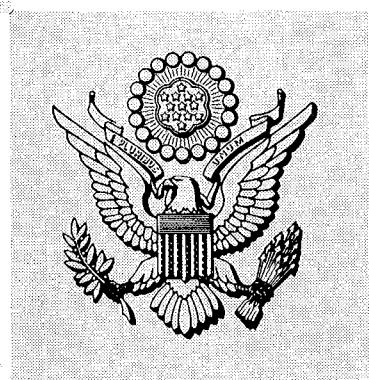


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MATHEMATICS RESEARCH CENTER

UNITED STATES ARMY



MRC Computer Program # 2

Interval Arithmetic Package

(INTERVAL)

Allen Reiter

Title: Interval Arithmetic Package (Interval)		
Programmer: Allen Reiter	Date: June 1965	MRC Program # 2
Program Language: FORTRAN-63/CODAP		Computer: CDC 1604
Other Info: COOP Organ. Code - WISC		

## 1.0 Purpose

To provide means for automatic error control by the computer by performing arithmetic operations on intervals containing the exact value, rather than on (approximate) real numbers of finite precision. This package is designed for use with FORTRAN '63, and enables the user to treat intervals as ordinary variables.

## 2.0 Usage

An interval number is a pair of real numbers  $(a, b)$ , with  $a \leq b$ . For a full description of the rules governing interval arithmetic, see [1]. Herein we describe how the user can use this package within the scope of the FORTRAN '63 compiler.

The user designates certain variables to be interval-valued by the use of TYPE - designation statements (see FORTRAN '63 reference manual, Chapter 5). Interval numbers are of type 5. Thus the statement

```
TYPE INT5 (2) X, Y, MU
```

designates the variables X, Y, and MU as being interval-valued. The compiler automatically assigns two consecutive storage cells (corresponding to the left end-point and the right end-point respectively of the interval) to each of the variables. The user then refers to all interval variables as he might to an ordinary (real-valued) variable, and uses them in arithmetic assignment statements just as if they were ordinary numbers. This usage, however, is subject to certain restrictions.

We give some examples. Let A, B, C, D all be of type INT5; let E, F, and G be of type REAL, and let K and L be of type INTEGER.

(a) The statement

$$A = C + D - 3 * (K/B - L * E)$$

causes the right-hand side of the equation to be evaluated in interval arithmetic, and the interval thus computed is stored in A.

(b) The statement

$$A = E * F + K$$

causes the right-hand side to be evaluated in real arithmetic. This result is then converted to interval form (i.e. an interval with both end-points being equal) and stored in A.

(c) The statement

$$E = L * A$$

will compute the interval  $L * A$ , find the mid-point of this interval, and store this in the (REAL) cell E.

(d) The statement

$$K = L * A$$

computes the interval  $L * A$ , finds its mid-point, truncates this value to an integer, and stores it in  $K$ .

Thus the three types INTEGER, REAL, and INT5 may be freely mixed. No other types may appear in a statement which contains a type INT5 variable, with one important exception.

(e) The statement

$$A = (a, b)$$

where  $a$  and  $b$  are floating-point constants such that  $b \geq a$  will assign to  $A$  the interval  $(a, b)$ . This works because the FORTRAN '63 compiler thinks that  $(a, b)$  is a complex number, and generates the proper calling sequence to the INTERVAL subroutine. See also the section on input-output.

(f) Arithmetic operations. Addition, subtraction, and multiplication in interval mode are straight-forward, governed by the rules described in [1]. Division likewise is straight-forward, except that an attempt to divide by an interval containing zero will produce an error indication (see section on errors.) Exponentiation, however, has several cases.

Let  $A$  and  $B$  be of type INT5,  $F$  and  $G$  of type REAL, and  $K$  and  $L$  of type INTEGER. The expression

$$A ** B$$

is an interval which is exactly the set of all values of  $x^y$ , where  $x$  ranges over  $A$  and  $y$  ranges over  $B$ . An error indication will be given if the left

end-point of A is not positive. The expressions

$$F^{**} B \quad \text{and} \quad K^{**} B$$

where F and K are positive also give the range respectively of  $F^y$  and  $K^y$  as y ranges over B. The expression

$$A^{**} L ; \quad L \geq 0$$

will give the product of A with itself taken L times; if zero is interior to A, then  $A^{**} L$  is an interval much wider than the range of values of  $x^L$  for x in A, and in any case always contains the latter. No error indication can be triggered by this expression. The expression

$$A^{**} F$$

where F is an integer in floating-point form ( $|F| \leq 2^{35}-1$ ) will give the exact range of values of  $x^F$ . No error indication will result if  $F > 0$ , even though the left end-point of A is negative. If F is negative and A contains zero, the error indicator will be triggered; as long as A does not contain zero, the exponentiation will take place. If F is not an exact integer (or if  $|F| > 2^{35}-1$ ), then  $A^{**} F$  is defined only as long as the left end-point of A is positive. The expression

$$A^{**} (-L) , \quad L > 0$$

is also defined only if

$$A^{**} L$$

does not contain zero, and is equal to  $1/(A^{**} L)$ .

(g) In addition to the arithmetic functions, the user is provided with the following elementary transcendental functions: LOGINT, EXPINT, SININT, COSINT, and ATANINT. The usage for all of them is the same; for example

```
CALL LOGINT ( ARGUMENT, ANSWER )
```

will assign to the interval ANSWER the range of values of  $\log(x)$  as  $x$  ranges over the interval ARGUMENT. LOGINT will produce an error indication if ARGUMENT has a non-positive left end-point; the other routines have no error indications.

### 2.3 External subroutines.

INTERVAL makes use of the following subroutines, all of which are part of the FORTRAN '63 system library:

	DLOG	DEXP
DSIN	DCOS	DATAN

### 2.7 Error processing.

The user has to reserve one cell of labeled common, by using the statement

```
COMMON/ OVERFLOW/ INDCATOR
```

for communication with the INTERVAL package. If an error condition is triggered by an attempt to perform an illegal operation (such as, for instance, attempting to divide by an interval containing zero) the contents of the cell INDCATOR are set to 1. The result of an illegal operation is not defined. Control flow follows the normal sequence: it is therefore up to the user to do something about the error condition, if it exists.

## 2.10 Input and Output.

FORTRAN '63, while making excellent provisions for evaluating TYPE OTHER arithmetic statements, has entirely neglected the question of getting the values of TYPE OTHER variables in/out of the computer. Fortunately, in our case, interval numbers formally are identical to complex numbers; that is, they both are of the form  $(a, b)$ . The user can therefore use the same input-output format as he would for TYPE COMPLEX numbers.

Example. Suppose  $X$  is an array of interval numbers, and it is desired to print out the first 12 values of  $X$ , four intervals per line. This can be accomplished by the statements

```
PRINT 1, (X(I), I = 1, 12)  
1      FORMAT (4C(F12.6, F12.6)).
```

There is an alternate method of doing this, which at the same time provides the means for getting at either end-point of an interval number separately. Consider the group of statements

```
TYPE INT5 (2) X  
DIMENSION X(12), XX(2,12)  
EQUIVALENCE (X, XX).
```

Here  $XX$  is an ordinary (TYPE REAL) variable; and for any index  $J$  the value of  $XX(1, J)$  corresponds to the value of the left end-point of  $X(J)$ , while  $XX(2, J)$  corresponds to the right end-point of  $X(J)$ . Thus, to print out the array  $X$  in the same format as above, the user can also give the command

```
PRINT 2, (XX(1,I),XX(2,I), I = 1,12)  
2      FORMAT (8F12.6)
```

All the above comments also apply, of course, to input statements as well.

2.13 Accuracy. Exact, and then some - see section on method.

2.14 Cautions to user.

- a) This program, like all MRC programs, is not designed for general utility. Rather, the motivation is to see how a certain scheme can be implemented on the computer, and also to test its usefulness in the context of various applications. The program is not guaranteed to be completely debugged. Also, it is anticipated that the user may have to change the program substantially to suit his own needs. For this purpose an appendix describing how the program works is included.
- b) It is well known that interval arithmetic is not distributive, and the user is cautioned that the way he writes a given statement may greatly affect the width of a computed interval. In general, it is advisable that the user obtain at all times a CODAP - language listing of the compiled version of his program, and wade through it to determine that FORTRAN '63 does indeed give the user what he thinks he should get. (For conventions regarding QnQ subroutines, see first of all the FORTRAN '63 reference manual, p. E-1 ff.)
- c) Integers in fixed-point form must not exceed  $2^{35}-1$  in absolute value.

## 2.17 References.

- [1] Ramon E. Moore, "The Automatic Analysis and Control of Error in Digital Computation Based on the Use of Interval Numbers". Error in Digital Computation, Volume I, (ed. L. B. Rall), John Wiley & Sons, Inc., 1965.
- [2] FORTRAN '63 Reference Manual for the 1604/1604-A. Control Data Corp., Pub. No. 60052900, Revision A, June 1964.

## 3.0 Method.

Arithmetic operations on interval numbers are performed such that the resulting interval is guaranteed to contain the exact answer. To accomplish this, the computed result for the left end-point is rounded "down", while that for the right end-point is rounded "up". The actual arithmetic operations are performed in fixed point, simulating the hardware floating-point. For increased accuracy, rounding is performed after normalization (unlike the hardware floating-point). The elementary functions are computed using the library double-precision routines, with the results rounded up or down to single precision as necessary. The whole process is quite slow - no timing figures are available at this moment.

## 4.0 Acknowledgement.

The author wishes to express his gratitude to Dr. R. E. Moore and Dr. H. J. Wertz for some very valuable suggestions, and especially to Mr. Augustine Chai for his aid in writing and debugging this package.

## APPENDIX

### Description of the program.

1. INTERVAL makes use of three arithmetic subroutines which simulate the hardware floating-point operations. These are SPOPSADD for simulated addition, SPOPSMUL for multiplication, and SPOPSDIV for division. Each subroutine expects, upon entry, to find its two arguments in the A and Q registers (for SPOPSDIV the dividend is in the A - register, while the divisor is in the Q- register). Upon exit, the lower bound for the result is in the Q-register, while A contains the upper bound. These routines are described in Fig. 1-4.
2. Exponentiation, as already indicated, has several subcases. The most general case, that of  $B^{**} C$  where C is not an integer, is handled simply by computing the answer as EXPINT (C \* LOGINT(B)). If the exponent is an integer in fixed point, exponentiation is handled by repeated multiplications in interval arithmetic (see figure 5). For exponents that are integers in floating-point form, repeated multiplication can still be used if the base does not contain zero. If the base does contain zero, we have to distinguish between even and odd exponents. For even exponents, the answer is given by the interval (0, M) where M is the larger in absolute value of the two end-points of the base raised to the proper power. For odd exponents, the answer is given by (N, M), where N is the power of the left end-point, and M the power of the right end-point. (N must be computed so that the result is rounded down, while M is rounded up.) The respective powers are again computed using repeated multiplication, this time on the "real" end-points.

See figure 5.

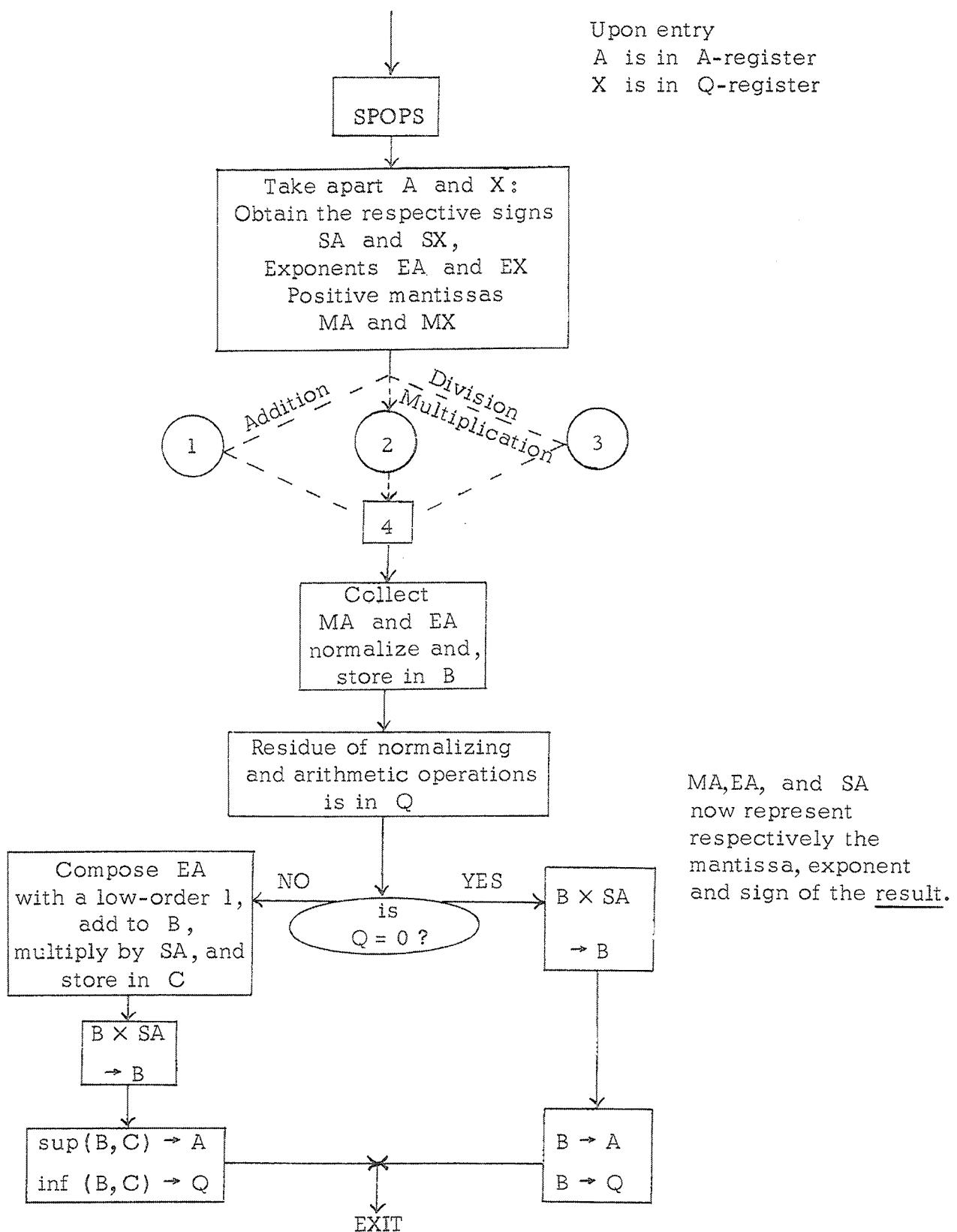


Figure 1 Flow Chart For SPOPS

ADDITION

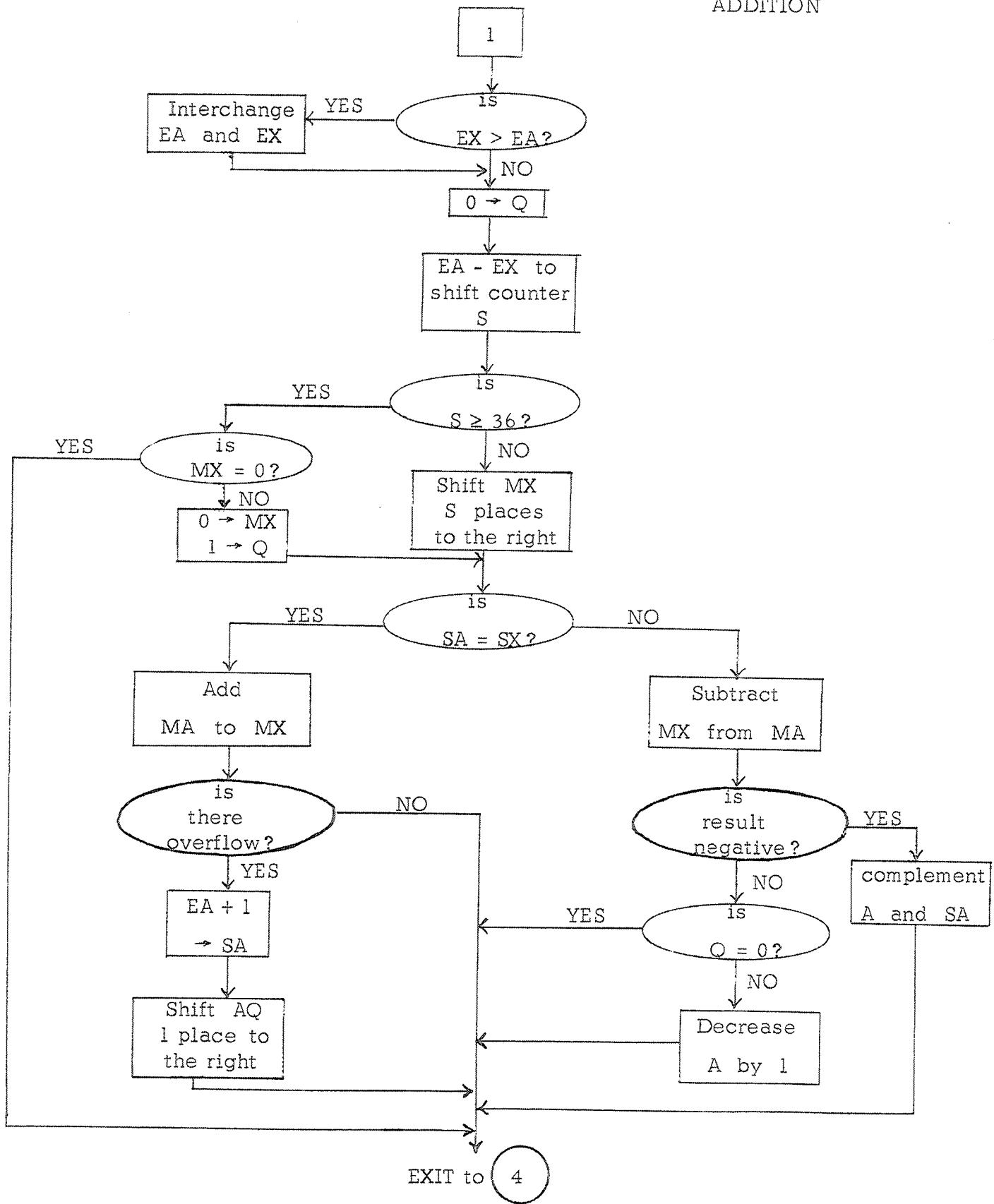


Figure 2 Flow Chart For Addition

MULTIPLICATION

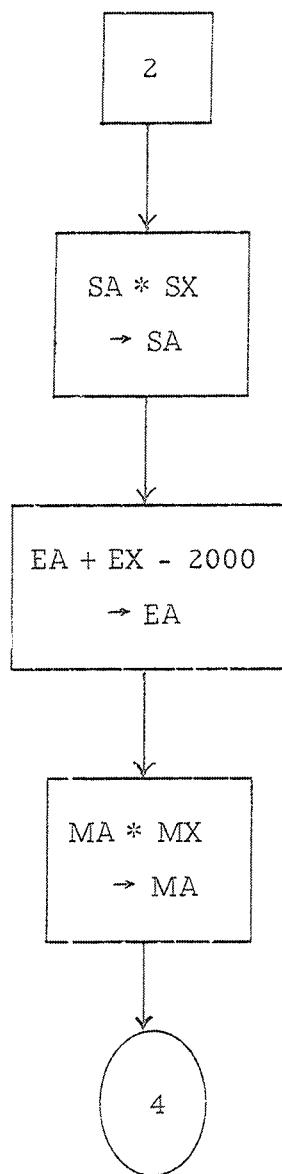


Figure 3 Flow Chart For Multiplication

DIVISION

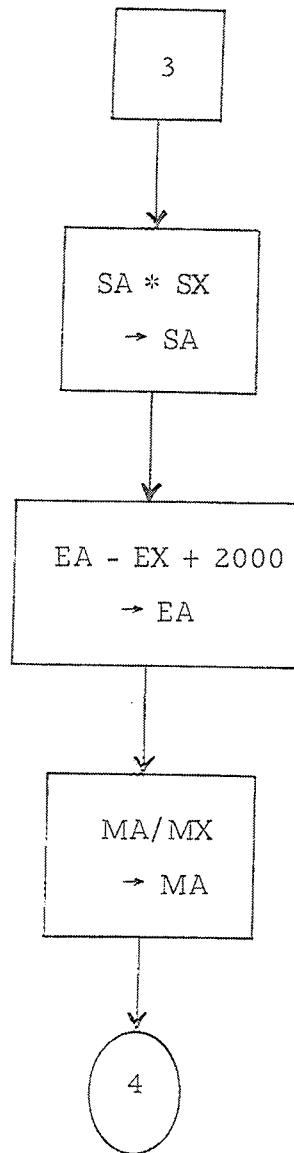


Figure 4 Flow Chart For Division

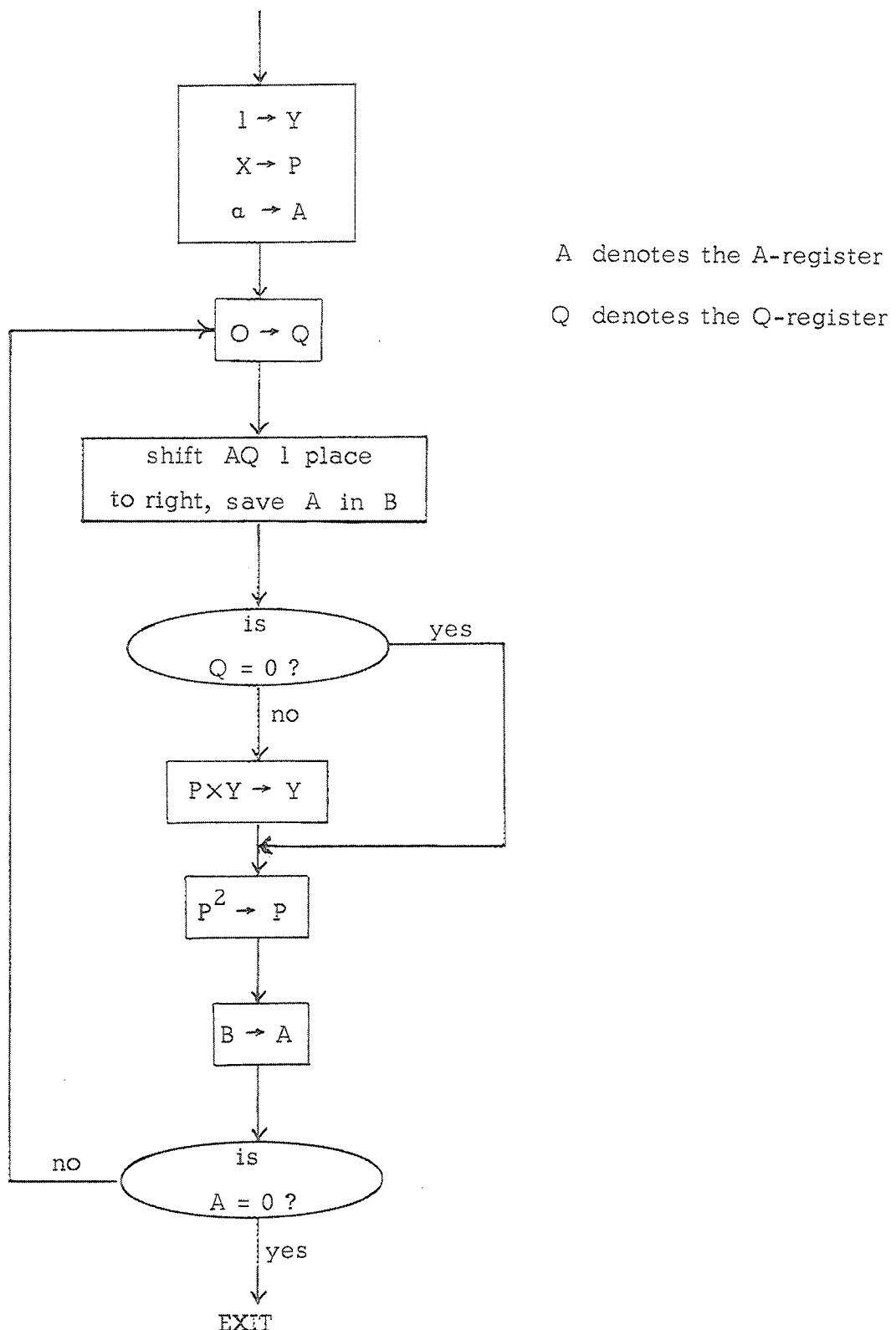


Figure 5 Algorithm for evaluating  $Y = X^a$  by repeated multiplications

INTERVAL ARITHMETIC PACKAGE

```

IDENT INTERVAL ARITHMETIC PACKAGE
EXT INTERVAL
EXT ERRORINT
EXT LOGIN
EXT EXPINT
EXT SPOPSADD
EXT SPOPSMUL
EXT SPOPSDIV
A B TEMP
BSS BSS BSS
ENTRY ENTRY ENTRY
BSS BSS BSS
2 2 2
ARGUMENTS
4 4 4
Q1Q00530 Q1Q00530 Q1Q00530
C C C
ENTRY ENTRY ENTRY
SLJ SLJ SLJ
** ** **
LDA LDA LDA
7 7 7
STA STA STA
=SEL =SEL =SEL
LDA 7 A+1
STA =SER
SLJ EXIT
ENTRY EXIT
Q1Q01530 Q1Q01530 Q1Q01530
BSS BSS BSS
ENTRY ENTRY ENTRY
SLJ SLJ SLJ
** ** **
Q1Q01550 Q1Q01550 Q1Q01550
LOAD NEGATIVE WITH INTERVAL
LDA WITH INTEGER
INTVL001
INTVL002
INTVL003
INTVL004
INTVL005
INTVL006
INTVL007
INTVL008
INTVL009
INTVL010
INTVL011
INTVL012
INTVL013
INTVL014
INTVL015
INTVL016
INTVL017
INTVL018
INTVL019
INTVL020
INTVL021
INTVL022
INTVL023
INTVL024
INTVL025
INTVL026
INTVL027
INTVL028
INTVL029
INTVL030
INTVL031
INTVL032
INTVL033
INTVL034
INTVL035

```

	SLJ	Q005	INTVL036
	ENTRY	Q1Q01500	INTVL037
Q1Q01500	SLJ	**	INTVL038
	RTJ	IN1	INTVL039
	LAC	7 A	INTVL040
	SLJ	Q054	INTVL041
	ENTRY	Q1Q00510	INTVL042
Q1Q00510	SLJ	**	INTVL043
	RTJ	IN1	INTVL044
	LDA	7 A	INTVL045
	SLJ	Q05	INTVL046
	ENTRY	Q1Q01510	INTVL047
Q1Q01510	SLJ	**	INTVL048
	RTJ	IN1	INTVL049
	LAC	7 A	INTVL050
	SLJ	Q05	INTVL051
	ENTRY	Q1Q02500	INTVL052
	SLJ	**	INTVL053
	RTJ	IN1	INTVL054
ADINT	LDA	7 A	INTVL055
ADREAL	RTJ	FLOCAT	INTVL056
+	STA	=SSAVA	INTVL057
	LDQ	EL	INTVL058
	RTJ	SPOPSADD	INTVL059
	STQ	EL	INTVL060
	LDA	SAVA	INTVL061
	LDQ	ER	INTVL062
	RTJ	SPOPSADD	INTVL063
	SLJ	Q005	INTVL064
	ENTRY	Q1Q03500	INTVL065
Q1Q03500	SLJ	**	INTVL066
	RTJ	IN1	INTVL067
	LAC	7 A	INTVL068
	SLJ	ADINT	INTVL069
	ENTRY	Q1Q02510	INTVL070
			ADD REAL

Q1Q02510	SLJ	**	INTVL071
	RTJ	TNL	INTVL072
	LDA	A	INTVL073
	SLJ	ADREAL	INTVL074
Q1G03510	ENTRY	**	INTVL075
	SLJ	IN1	INTVL076
	RTJ	A	INTVL077
	LAC	ADREAL	INTVL078
	SLJ	IN1	INTVL079
Q1Q02530	BSS	0	INTVL080
	ENTRY	Q1Q02550	INTVL081
	SLJ	**	INTVL082
	RTJ	IN1	INTVL083
AD55	LDA	A	INTVL084
+	LDQ	7	INTVL085
	RTJ	A+1	INTVL086
AD5	STQ	EL	INTVL087
	LDA	ER	INTVL088
	LDQ	SPOPSADD	INTVL089
	RTJ	EL	INTVL090
	SLJ	Q005	INTVL091
Q1Q03530	ENTRY	Q1Q03530	INTVL092
	BSS	0	INTVL093
Q1Q03550	ENTRY	Q1Q03550	INTVL094
	SLJ	**	INTVL095
	RTJ	IN1	INTVL096
	LAC	A+1	INTVL097
SUB55	+	EL	INTVL098
	LDQ	SPCPSADD	INTVL099
	RTJ	EL	INTVL100
	LAC	7	INTVL101
	SLJ	A	INTVL102
	STQ	AD5	INTVL103
	LAC	7	INTVL104
Q1Q02050	ENTRY	**	INTVL105
	SLJ		ADD TO INTEGER

		RTJ	IN1	INTVL106
		RTJ	FLOAT	INTVL107
Q205		STA	EL	INTVL108
		SLJ	ER	INTVL109
		ENTRY	AD55	INTVL110
Q1Q03050		SLJ	Q1Q03050	INTVL111
		RTJ	**	INTVL112
Q305		STA	IN1	INTVL113
		SLJ	FLCAT	INTVL114
		STA	EL	INTVL115
		SLJ	ER	INTVL116
		ENTRY	SUB55	INTVL117
Q1Q02150		SLJ	Q1Q02150	INTVL118
		RTJ	**	INTVL119
		SLJ	IN1	INTVL120
		ENTRY	Q205	INTVL121
Q1Q03150		SLJ	Q1Q03150	INTVL122
		RTJ	**	INTVL123
		SLJ	IN1	INTVL124
		ENTRY	Q305	INTVL125
Q1Q04500		SLJ	Q1Q04500	INTVL126
		RTJ	**	INTVL127
		LDA	IN1	INTVL128
Q0315		RTJ	A	INTVL129
		STA	FLOAT	INTVL129
		STA	=SDL	INTVL130
MULT		LDA	=SDR	INTVL131
+		LDQ	DL	INTVL132
		RTJ	EL	INTVL133
		STA	SPOPSMUL	INTVL134
		STQ	=STMUX	INTVL135
		LDA	=STMUX	INTVL136
		LDQ	DL	INTVL137
		RTJ	ER	INTVL138
		LDQ	SPOPSMUL	INTVL139
+		RTJ		INTVL140

RTJ	MAXMIN	INTVL141
LDA	DR	INTVL142
LDQ	EL	INTVL143
RTJ	SPOP SMUL	INTVL144
RTJ	MAXMIN	INTVL145
LDA	DR	INTVL146
LDQ	ER	INTVL147
RTJ	SPOP SMUL	INTVL148
RTJ	MAXMIN	INTVL149
LDA	TMIN	INTVL150
STA	EL	INTVL151
LDA	TMAX	INTVL152
SLJ	Q005	INTVL153
ENTRY	Q1Q04510	INTVL154
SLJ	**	INTVL155
RTJ	IN1	INTVL156
+	A	INTVL157
LDA	Q0315	INTVL158
SLJ	Q1Q04530	INTVL159
ENTRY	0	INTVL160
Q1Q04530	BSS	INTVL161
SLJ	Q1Q04550	INTVL162
RTJ	**	INTVL163
Q0455	ENTRY	INTVL164
RTJ	Q1Q04550	INTVL165
LDA	IN1	INTVL166
STA	A	INTVL167
STA	=SDL	INTVL168
LDA	A+1	INTVL169
STA	=SDR	INTVL170
SLJ	MULT	INTVL171
ENTRY	Q1Q04050	INTVL172
SLJ	**	INTVL173
Q1Q04050	INTVL174	INTVL174
RTJ	FLOAT	INTVL175
RTJ	EL	
SMUL	ER	
STA	Q0455	
SLJ		

MULTIPLY REAL BY INTERVAL

Q1Q04150	ENTRY	Q1Q04150
	SLJ	**
	RTJ	**
+	SLJ	IN1
	ENTRY	SMUL
	BSS	Q1Q0530
Q1Q0530	ENTRY	0
Q1Q05550	SLJ	Q1Q05550
Q555	RTJ	**
	LDA	IN1
	AJP	T
	AJP	A
	LDA	ERRDIV
	AJP	Q5551
	AJP	2
	LDA	A+1
	LDQ	7
	RTJ	ERRDIV
	RTJ	2
	LDA	ERRDIV
	STQ	F1
	LDA	A+1
	LDQ	SPOPSDIV
	RTJ	DL
	RTJ	F1
	LDA	A
	LDQ	SPOPSDIV
	RTJ	DR
	STA	MULT
	SLJ	Q1Q05500
	ENTRY	**
Q1Q05500	SLJ	DIVIDE BY INTEGER
	RTJ	IN1
	LDA	A
	RTJ	FLOAT
	STA	DL
	AJP	ERRDIV
	LDA	F1
	LDQ	DL
	RTJ	SPOPSDIV
	STQ	DL

TEST FOR DIVISION BY ZERO

INTVL176	INTVL177	INTVL178	INTVL179	INTVL180	INTVL181	INTVL182	INTVL183	INTVL184	INTVL185	INTVL186	INTVL187	INTVL188	INTVL189	INTVL190	INTVL191	INTVL192	INTVL193	INTVL194	INTVL195	INTVL196	INTVL197	INTVL198	INTVL199	INTVL200	INTVL201	INTVL202	INTVL203	INTVL204	INTVL205	INTVL206	INTVL207	INTVL208	INTVL209	INTVL210
----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------

```

        STA      DR
        SLJ      MULT
        ENTRY   Q1Q05510
        SLJ      DIVIDE BY REAL
        RTJ      INTVL211
        LDA      INTVL212
        SLJ      INTVL213
        7       INTVL214
        A       INTVL214
        Q055    INTVL215
        Q055    INTVL216
        Q055    INTVL217
        Q1Q05150  INTVL218
        SLJ      INTVL219
        RTJ      INTVL220
        STA      INTVL221
        ER      INTVL222
        SLJ      INTVL223
        ENTRY   INTVL224
        SLJ      INTVL225
        RTJ      INTVL226
        SLJ      INTVL227
        SLJ      INTVL228
        RTJ      INTVL229
        SLJ      INTVL230
        EXIT    INTVL230
        Q1Q10050  INTVL231
        SLJ      INTVL232
        RTJ      INTVL233
        RTJ      INTVL233
        SLJ      INTVL234
        ENTRY   INTVL235
        SLJ      INTVL236
        STA      INTVL236
        7       INTVL237
        A       INTVL238
        A+1     INTVL239
        EXIT    INTVL239
        Q1Q10150  INTVL240
        SLJ      INTVL240
        RTJ      INTVL241
        SLJ      INTVL242
        ENTRY   INTVL242
        SLJ      INTVL243
        RTJ      INTVL244
        LDA      INTVL245
        +
        SLJ      INTVL244
        ENTRY   INTVL244
        SLJ      INTVL245
        RTJ      INTVL245
        LDA      INTVL245
        +
        SLJ      INTVL245
        ENTRY   INTVL245
        SLJ      INTVL245
        RTJ      INTVL245
        LDA      INTVL245

```

```

        STA    7 A          INTVL246
        LDA    ER          INTVL247
        STA    7 A+1       INTVL248
        SLJ    EXIT        INTVL249
        ENTRY
Q1Q10510  SLJ    Q1Q10510  STORE INTERVAL IN REAL
        RTJ    EXIT        INTVL251
        LDA    INTNL252
        FAD    INTVL253
        FDV    INTVL254
        STA    INTVL255
        SLJ    INTVL256
        ENTRY
Q1Q10500  SLJ    EXIT        INTVL257
        RTJ    INTNL258
        LDA    INTVL259
        FAD    INTVL260
        FDV    INTVL261
        RTJ    INTVL262
        FIX    INTVL263
        STA    INTVL264
        SLJ    INTVL265
        ENTRY
Q0Q06500  SLJ    EXIT        INTVL266
        Q0Q06500  INTVL267
        **      INTVL268
        EXIT
        COMPLEMENT ACC
        SLJ    INTVL269
        LAC    INTVL270
        STQ    INTVL271
        STA    INTVL272
        ER     INTVL273
        SLJ    INTVL274
        ENTRY
Q2Q07055  BSS    Q2Q07055, Q2Q07155, Q2Q07555  INTERVAL EXPONENT - IREAL
        0      SET OF POWERS
        Q2Q07155 BSS    INTVL275
        Q2Q07555 SLJ    INTVL276
        RTJ    INTVL277
        LDA    INTVL278
        STA    INTVL279
        SLJ    INTVL280

```

```

Q071          LDA    7 A           INTVL281
              STA    TEMP+2   INTVL282
              LDA    B        INTVL283
              AJP    ERREXP   INTVL284
              3     ERREXP   INTVL285
              STA    B        INTVL286
              LDA    STA    INTVL287
              STA    RTJ    INTVL288
              RTJ    ZRO    INTVL289
              ZRO    LOGINT INTVL290
              +     7 3       TEMP=LOG(BASE)
              +     ZRO    Q1Q00550  INTVL291
              ZRO    TEMP+2   INTVL292
              RTJ    Q1Q04550  INTVL293
              ZRO    TEMP    Q1Q10550  INTVL294
              RTJ    TEMP    TEMP*TEMP  INTVL295
              ZRO    TEMP    TEMP*EXPOENT TO ACC
              RTJ    EXPINT TEMP=EXPONENT * LOG(BASE)
              ZRO    TEMP    TEMP+2=EXPF(TEMP)
              ZRO    RTJ    TEMP+2=TEMP+2 TC ACC
              SLJ    RTJ    TEMP+2 TC ACC
              +     SLJ    EX2     INTVL298
              ERREXP  ENTRY   INTVL299
              Q2Q07015 BSS    INTVL300
              Q2Q07115 BSS    INTVL301
              Q2Q07515 SLJ    INTVL302
              RTJ    EX2     INTVL303
              ERRORINT ENTRY   INTVL304
              Q2Q07115, Q2Q07515, Q2Q07015 INTVL305
              0      0      INTVL306
              0      0      INTVL307
              0      0      INTVL308
              **    IN2      INTVL309
              REAL TO INTERVAL POWER
              INTVL310
              INTVL311
              INTVL312
              INTVL313
              INTVL314
              INTVL315

```



```

PAS S          ZRO      TEMP+2      INTVL351
                  RTJ      Q1Q00550    INTVL352
                  ZRO      TEMP      INTVL353
                  RTJ      Q1Q04550    INTVL354
                  ZRO      TEMP      INTVL355
                  RTJ      Q1Q10550    INTVL356
                  ZRO      TEMP      INTVL357
                  LDA      J        INTVL358
                  AJP      LOOP     INTVL359
                  LDG      EXPQS    INTVL360
                  QJP      EXPQS    INTVL361
                  RTJ      Q1Q00510    INTVL362
                  ZRO      F1       INTVL363
                  RTJ      Q1Q05550    INTVL364
                  ZRO      SLJ      TEMP+2    INTVL365
                  RTJ      EX2       FDV B   INTVL366
                  EXPQS    Q1Q00550    INTVL367
                  ZRO      SLJ      TEMP+2    INTVL368
                  SLJ      EX2       LDA B   INTVL369
                  TESTEXP   AJP      EXIT     INTVL369
                  ENQ      SLJ      CHECK FOR NEGATIVE EXPONENT
                  SCM      AJP      -1 FOR NEGATIVE EXPONENT
                  STQ      =SSEXP    INTVL370
                  SLJ      TESTEXP   COMPLEMENT A
                  POSEXP    2        INTVL371
                  ENQ      SLJ      -1
                  ALSEV    POSEXP    INTVL372
                  =SSEXP    TESTEXP   INTVL373
                  SLJ      TESTEXP   INTVL374
                  ENQ      SLJ      +1 FOR POSITIVE EXPONENT
                  POSEXP    1        INTVL375
                  ENQ      ENTRY    INTVL376
                  SLJ      Q2Q07553   INTVL377
                  BSS      0        INTVL378
                  ENTRY    Q2Q07051,Q2Q07151,Q2Q07551 REAL EXPONENT - SET OF P
                  BSS      0        INTVL379
                  Q2Q07051 BSS      0        INTVL380
                  Q2Q07151 BSS      0        INTVL381
                  Q2Q07551 SLJ      **      INTVL382
                  RTJ      IN2       INTVL383
                  LDA      A        INTVL384
                  +       7

```

		RTJ	FIX	INTV L386
		STA	= SK1	INTV L387
		RTJ	FLOAT	INTV L388
+		FSB	A	INTV L389
		AJP	WHOLE	INTV L390
		LDA		INTV L391
		STA		INTV L392
		SLJ		INTV L393
		LDA		INTV L394
		RTJ		INTV L395
		LDQ		INTV L396
		QJP	TEST EXP	INTV L397
		LDQ		INTV L398
		QJP		INTV L399
		STA		INTV L400
		ENQ		INTV L401
		LDL		INTV L402
		AJP		INTV L403
		LDA		INTV L404
		LDQ		INTV L405
		RTJ		INTV L406
	+	STQ		INTV L407
		SLJ		INTV L408
		LDQ		INTV L409
		STQ		INTV L410
		LDQ		INTV L411
		7		INTV L412
		K1		
		EVEN		
		K1		
		B		
		REPEAT1	ODD EXPONENT	INTV L413
		TEMP+1	OBTAIN LEFT END-POINT BY	INTV L414
		REPEAT2	REPEATED MULTIPLICATIONS	INTV L415
		TEMP+1		INTV L416
		TEMP+2		INTV L417
		B+1		INTV L418
		7		INTV L419
		K1		INTV L420
		REPEAT1	OBTAİN RIGHT ENDPOINT	
		TEMP+1		
		REPEAT2		
		TEMP+1		
		TEMP+3		
		ENDEXP		
		0		
		EVEN	EVEN EXPONENT	
		STQ	SET LEFT END-POINT TO ZERO	

```

LAC      7      B
LDQ      7      B+1
STQ      TEMP+3
THS      TEMP+3
STA      TEMP+3
LDQ      TEMP+3
SLJ      UPPER
REPEAT1
SLJ      **
STQ      TEMP
LDQ      F1
STQ      TEMP+1
LRS      0
ENQ      P=1.
LRS      J=J/2
STA      REPEAT2
QJP      SKIP MULTIPLICATION OF P BY Y
LDA      IF LCW-ORDER BIT OF J IS ZERO
LDQ      TEMP+1
RTJ      P=P*Y
SLJ      EXIT TO STA/STQ
LDA      REPEAT1
LDQ      TEMP
RTJ      TEMP
LDQ      SPOPSMUL
STQ      TEMP
LDA      SPOPSMUL
RTJ      Y=Y*Y
TEMP      Y=Y*Y
LDA      REPEAT1
AJP      END IF J=0
LDA      SET UP EXIT
ARS      24
INA      1
LDA      LOOPLOOP
REPEAT1
INA      1
INA      1
SAU      EXIT
SLJ      INITIALIZATION FOR Q1Q
INL      1
SIU      END1
LIU      1
INI      -1
SIU      *+1
INTVL421
INTVL422
INTVL423
INTVL424
INTVL425
INTVL426
INTVL427
INTVL428
INTVL429
INTVL430
INTVL431
INTVL432
INTVL433
INTVL434
INTVL435
INTVL436
INTVL437
INTVL438
INTVL439
INTVL440
INTVL441
INTVL442
INTVL443
INTVL444
INTVL445
INTVL446
INTVL447
INTVL448
INTVL449
INTVL450
INTVL451
INTVL452
INTVL453
INTVL454
INTVL455

```

FIND BIGGER (IN ABSOLUTE  
VALUE) OF THE TWO END-POINTS

ADD. OF CALL TO INL



```

AJP 3 FIXNEG EXIT FOR NEGATIVE POSITION COEFFICIENT IN A
FAD HEXP INTVL491
SCL HEXP INTVL492
SLJ FIX INTVL493
FSB HEXP INTVL494
SST HEXP INTVL495
SLJ FIX INTVL496
HEXP INTVL497
FLOAT SLJ OCT INTVL498
AJP 3 ** SLJ INTVL499
      FLN SST INTVL500
      FAD SLJ INTVL501
      SCL FAD INTVL502
      SLJ FLOAT INTVL503
      SCL PZERO INTVL504
      FAD SLJ INTVL505
      SLJ FLOAT INTVL506
      SLJ ** INTVL507
      THS TEST FOR MAXIMUM
      STA A•GT•TMAX
      STQ =SSAVQ INTVL508
      LDA SAVQ INTVL509
      THS TEST FOR MINIMUM
      SLJ INTVL510
      STA INTVL511
      SLJ INTVL512
      STA INTVL513
      SLJ INTVL514
      TMIN INTVL515
      MAXMIN INTVL516
      TMIN INTVL517
      MAXMIN INTVL518
      -0 INTVL519
      OCT INTVL520
      MON INTVL521
      PZERO OCT INTVL522
      OCT INTVL523
      F1 0 INTVL524
      OCT 2044000000000000 INTVL525
      F1 0 INTVL526
      OCT 2001400000000000 INTVL527
      END IDENT ERRORINT INTERVAL ARITHMETIC PACKAGE
      ENTRY ERRORINT OVERFLOW INDICATOR CELL
      BLOCK 1 COMMON ORGR *

```

OVERFLOW INDICATOR CELL



```

X=ARG(2)
ANS(2)=PEP*DEXP(X)
RETURN
ENTRY COSINT
AL=ARG(1)
AR=ARG(2)
K=AL/PI2
IF(AL)101,102,102
101 K=K-1
102 AL=AL-K*PI2
AR=AR-K*PI2
T1=AL-PI
IF(T1)110,110,120
110 T1=AR-PI2
IF(T1)112,111,111
111 ANS(1)=-1.
ANS(2)=1.
RETURN
112 T1=AR-PI
IF(T1)113,114,114
113 LI=DCOS(AR)
RI=DCOS(AL)
GO TO 3
114 ANS(1)=-1.
T1=DCOS(AL)
T2=DCOS(AR)
IF(T1-T2)115,115,116
115 RI=T2
GO TO 7
116 RI=T1
GO TO 7
120 T1=AR-PI3
IF(T1)121,111,111
121 T1=AR-PI2
IF(T1)122,123,123

```

INTVL561  
INTVL562  
INTVL563  
INTVL564  
INTVL565  
INTVL566  
INTVL567  
INTVL568  
INTVL569  
INTVL570  
INTVL571  
INTVL572  
INTVL573  
INTVL574  
INTVL575  
INTVL576  
INTVL577  
INTVL578  
INTVL579  
INTVL580  
INTVL581  
INTVL582  
INTVL583  
INTVL584  
INTVL585  
INTVL586  
INTVL587  
INTVL588  
INTVL589  
INTVL590  
INTVL591  
INTVL592  
INTVL593  
INTVL594  
INTVL595

```

122 LI=DCOS(AL)
    RI=DCOS(AR)
    GO TO 3
123 ANS(2)=1°
    T1=DCOS(AL)
    T2=DCOS(AR)
    IF(T1-T2)124,124,125
124 LI=T1
    GO TO 126
125 LI=T2
    GO TO 126
126 IF((LI)127,128,129
127 ANS(1)=PEP*LI
    RETURN
128 ANS(1)=-EP
    RETURN
129 ANS(1)=MEP*LI
    RETURN
ENTRY SININT
AL=1.5707963267-ARG(2)
AR=1.5707963268-ARG(1)
GO TO 100
ENTRY ATANINT
X=ARG(1)
LI=DATAN(X)
X=ARG(2)
RI=DATAN(X)
GO TO 3
END IDENT SPOPS
** =SX
3 NEGA
AJP =O1
LDQ =SSA
RET1 STQ M12
LQC INTVL596
INTVL597
INTVL598
INTVL599
INTVL600
INTVL601
INTVL602
INTVL603
INTVL604
INTVL605
INTVL606
INTVL607
INTVL608
INTVL609
INTVL610
INTVL611
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INTVL615
INTVL616
INTVL617
INTVL618
INTVL619
INTVL620
INTVL621
INTVL622
INTVL623
INTVL624
INTVL625
INTVL626
INTVL627
INTVL628
INTVL629
INTVL630

```

SPECIAL OPERATIONS FOR THE  
 INTERVAL PACKAGE. ARGUMENTS ARE INTVL625  
 IN A AND Q. ANSWERS ARE - INTVL626  
 UPPER LIMIT IN A, INTVL627  
 LOWER LIMIT IN Q. INTVL628  
 STORE SIGN(A) INTVL629  
 INTVL630

STL =SMA  
 ARS 36  
 INA -2000B  
 AJP \*+2  
 AJP \*+1  
 2 IF NEGATIVE - INCREASE BY 1  
 INA 1  
 STA =SEA  
 LDA X  
 AJP 3  
 LDG NEGX  
 STQ =01  
 LQC =SSX  
 N12 MANTISSA(X)  
 STL =SMX  
 ARS 36  
 INA -2000B  
 AJP \*+2  
 AJP \*+1  
 2 IF NEGATIVE - INCREASE BY 1  
 INA 1  
 STA =SEX  
 SLJ INIT  
 SCM ALSEV  
 LQC =01  
 SLJ RET1  
 SCM ALSEV  
 LQC =01  
 SLJ RET2  
 M12 OCT 777777777777  
 ALSFV OCT -0  
 N12 OCT 777777777777  
 N13 OCT 40007777777777  
 HERD SLJ \*\*  
 STA =STEMP  
 LDA EA  
 AJP \*+2  
 AJP 2 \*+1

MANTISSA(A) INTVL631  
 INTVL632  
 INTVL633  
 INTVL634  
 INTVL635  
 INTVL636  
 INTVL637  
 INTVL638  
 INTVL639  
 INTVL640  
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 INTVL662  
 INTVL663  
 INTVL664  
 INTVL665

INA	-1	INTVL666
INA	2000B	INTVL667
ALS	36	INTVL668
SCL	N13	INTVL669
STA	EA	INTVL670
SST	TEMP	INTVL671
QJP	NO CARRY	INTVL672
FAD	=00	INTVL673
STA	=SSAVA	INTVL674
MUI	SA	INTVL675
STA	=SSAVB	INTVL676
LDA	EA	INTVL677
INA	1	INTVL678
FAD	SAVA	INTVL679
MUI	SA	INTVL680
AJP	FLIP	INTVL681
LDO	SAVB	INTVL682
SLJ	HERD	INTVL683
LRS	48	INTVL684
LDA	SAVB	INTVL685
SLJ	HERD	INTVL686
MUI	SA	INTVL687
FAD	=00	INTVL688
STA	SAVB	INTVL689
SLJ	RET3	INTVL690
ENTRY	SPOPSADD	INTVL691
SLJ	**	INTVL692
RTJ	INIT	INTVL693
LDA	EA	INTVL694
SUB	EX	INTVL695
AJP	3	INTVL696
ENQ	SWITCH	INTVL697
RET <sub>4</sub>	0	INTVL698
+	=0110	INTVL699
THS	SKIPS	INTVL700
SLJ	*+1	
REASON	SAU	
	SHIFT ADDRESS=EA-EX	

EXPONENT WITH LOW-ORDER 1  
ADD TO CHOPPED NUMBER FOR UPPER  
MULTIPLY BY SIGN

A TO Q  
CHOPPED NUMBER TO A

EXACT NUMBER TO A

AND Q

SP. ADDITION

GO TO INITIALIZE

EA-EX  
SWITCH IF EX.GT.EA

LJA MX  
 LRS \*\*  
 STA MX  
 LDA SA  
 ADD SX  
 AJP SUBTRACT  
 LDA MA  
 ADD MX  
 STA =STEMP  
 SCL N12  
 AJP NOVER  
 LDA EA  
 INA I  
 STA FA  
 LDA TEMP  
 LRS I  
 RTJ HERD  
 + SLJ SPOPSADD  
 LDA MX  
 SKIPS AJP  
 ENQ RET8  
 2 O  
 ENA  
 SLJ RET8  
 LDA TEMP  
 SLJ RET6  
 SUBTRACT MA  
 SUB MX  
 AJP 3 NEGATE  
 QJP RET6  
 INA -1  
 STO =SSSTCRE  
 AJP 2 \*+1  
 ENA 0  
 LQC STORE  
 SIU 1 GOOUT

LINE UP MX WITH MA INTVL701  
 INTVL702  
 COMPARE SIGNS OF X AND A INTVL703  
 INTVL704  
 INTVL705  
 INTVL706  
 INTVL707  
 INTVL708  
 INTVL709  
 SUBTRACT IF DIFFERENT SIGNS INTVL710  
 INTVL711  
 INTVL712  
 INTVL713  
 INTVL714  
 INTVL715  
 INTVL716  
 INTVL717  
 INTVL718  
 INTVL719  
 INTVL720  
 INTVL721  
 INTVL722  
 INTVL723  
 INTVL724  
 INTVL725  
 INTVL726  
 INTVL727  
 INTVL728  
 INTVL729  
 INTVL730  
 INTVL731  
 INTVL732  
 INTVL733  
 INTVL734  
 INTVL735

```

+ ENT 1 1 83
SIU 1 *+1
SCQ 1 **
LRS 11
STA TEMP
LDA EA
INA -72
STA EA
ENI EA
SLJ **
NEGATE NOVER
LQC SA
STQ SA
SCM ALSEV
ENQ 0
SLJ RET6
STA TEMP
LDA EA
LDQ EX
STA EX
STQ EA
LDA MA
LDQ MX
STQ MA
STA MX
LDA SX
LDQ SA
STA SA
STQ SX
LAC TEMP
RET4
SPOPSMUL SP•MULTIPLICATION
ENTRY SPOPSMUL
SLJ **
INIT RTJ
LDA EA
ADD EX
CHANGE SIGN OF RESULT
COMPLEMENT ACC
NO RESIDUE IN Q POSSIBLE
INTERCHANGE X AND A
INTVL736
INTVL737
INTVL738
INTVL739
INTVL740
INTVL741
INTVL742
INTVL743
INTVL744
INTVL745
INTVL746
INTVL747
INTVL748
INTVL749
INTVL750
INTVL751
INTVL752
INTVL753
INTVL754
INTVL755
INTVL756
INTVL757
INTVL758
INTVL759
INTVL760
INTVL761
INTVL762
INTVL763
INTVL764
INTVL765
INTVL766
INTVL767
INTVL768
INTVL769
INTVL770

```

STA	EA	NEW EXPONENT
LDA	SA	INTVL771
MUI	SX	INTVL772
STA	SA	INTVL773
LDA	MX	INTVL774
ALS	11	INTVL775
MUF	MA	INTVL776
RTJ	ADJUST	INTVL777
+ RTJ	HERD	INTVL778
SLJ	SP0PSMUL	INTVL779
ENTRY	SP0PSDIV	INTVL780
SP0PSDIV	SLJ	INTVL781
RTJ	RTJ	INTVL782
LDA	LDA	INTVL783
SUB	INA	INTVL784
EA	EA	INTVL785
EX	EX	INTVL786
INIT	EA	INTVL787
EA	EA	INTVL788
SA	SA	INTVL789
SX	SX	INTVL790
SA	SA	INTVL791
MX	MX	INTVL792
11	11	INTVL793
TEMP	TEMP	INTVL794
MA	MA	INTVL795
O	O	INTVL796
1	1	INTVL797
TEMP	TEMP	INTVL798
ADJUST	ADJUST	INTVL799
HERD	HERD	INTVL799
RTJ	RTJ	INTVL800
SLJ	SP0PSDIV	INTVL801
OCT	37777777777	INTVL802
SLJ	**	INTVL803
TEMP	TEMP	INTVL804
MASKMM	MASKMM	INTVL805
ADJUST	ADJUSTA	
AJP	1	

DIVIDE-NO OVERFLOW POSSIBLE

RSO  
LDA  
LLS  
SLJ  
LDA  
SLJ  
END

EA  
TEMP  
1  
ADJUST  
TEMP  
ADJUST

INTVL806  
INTVL807  
INTVL808  
INTVL809  
INTVL810  
INTVL811  
INTVL812